

10a : ELECTRONIC CONTROL UNIT

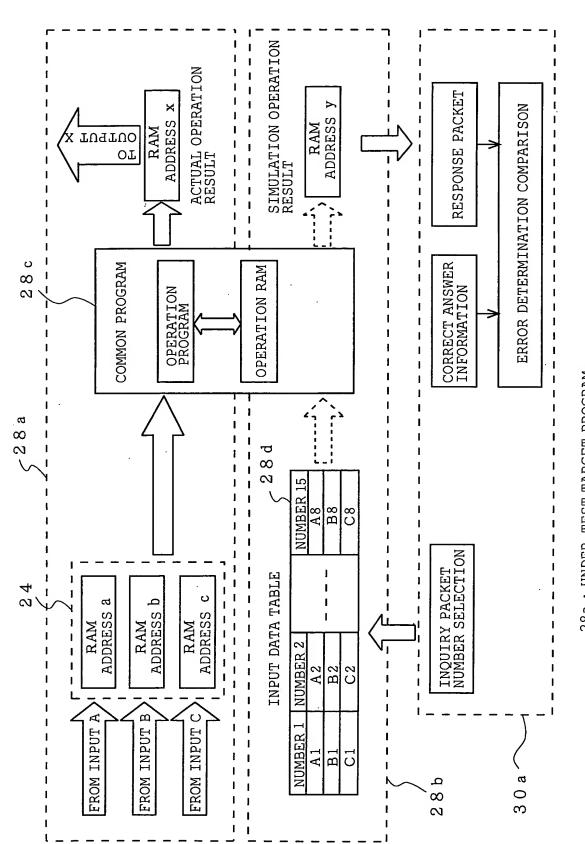
25a: NON-VOLATILE PROGRAM MEMORY

29, 49: SERIAL INTERFACE CIRCUIT

30a: MONITORING CONTROL CIRCUIT SECTION

40 : DATA PROCESSING CIRCUIT

WD: WATCHDOG SIGNAL

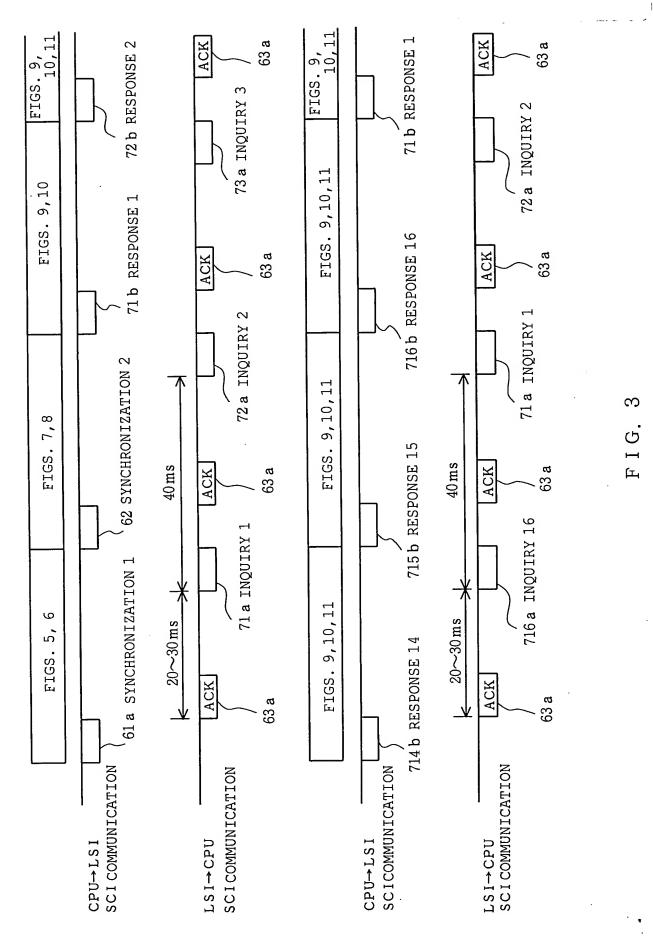


28a : UNDER-TEST TARGET PROGRAM
28b : SIMULATION-OPERATION-EXECUTION PROCESSING PROGRAM
30a : MONITORING CONTROL CIRCUIT SECTION

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61b  STX 55H  COMMAND 10H  ADDRESS 02H  MODE DATA 00H  ETX AAH  CHECKSUM  INQUIRY TRANSMISSION	71b~716b  RESPONSE PACKET STX 55H COMMAND 10H ADDRESS 1EH ANSWER DATA ***H ETX AAH CHECKSUM FORMAL RESPONSE
CPU→LSI  SYNCHRONIZATION PACKET 1  STX 55H  COMMAND 10H  ADDRESS 02H  MODE DATA 01H  ETX AAH  CHECKSUM  INQUIRY TRANSMISSION PERMISSION	SYNCHRONIZATION PACKET 2 STX 55H COMMAND 10H ADDRESS 1EH MODE DATA FFH ETX AAH CHECKSUM FIRST DUMMY RESPONSE
	SUM SEQUEST
10N ION	63b  CONFIRMATION PACKET  STX 55H  NACK 62H  SPECIFIED ADDRESS  ETX AAH  CHECKSUM  RETRANSMISSION REQUEST

FIG. 4

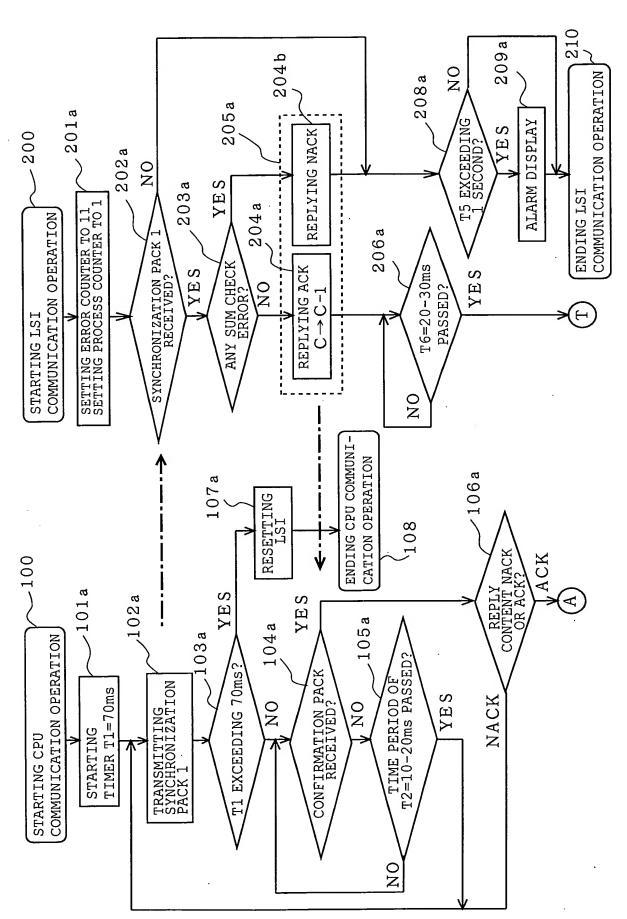
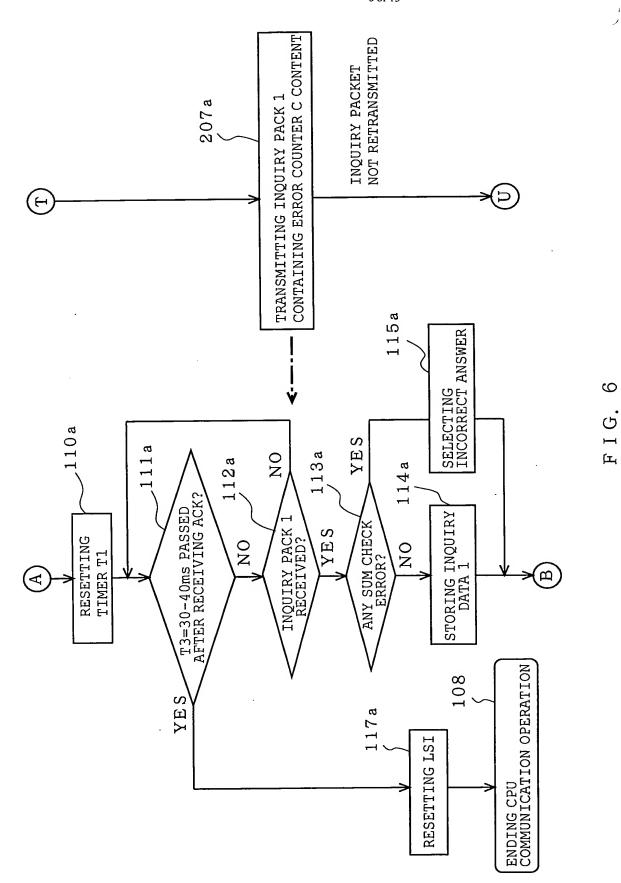


FIG. 5



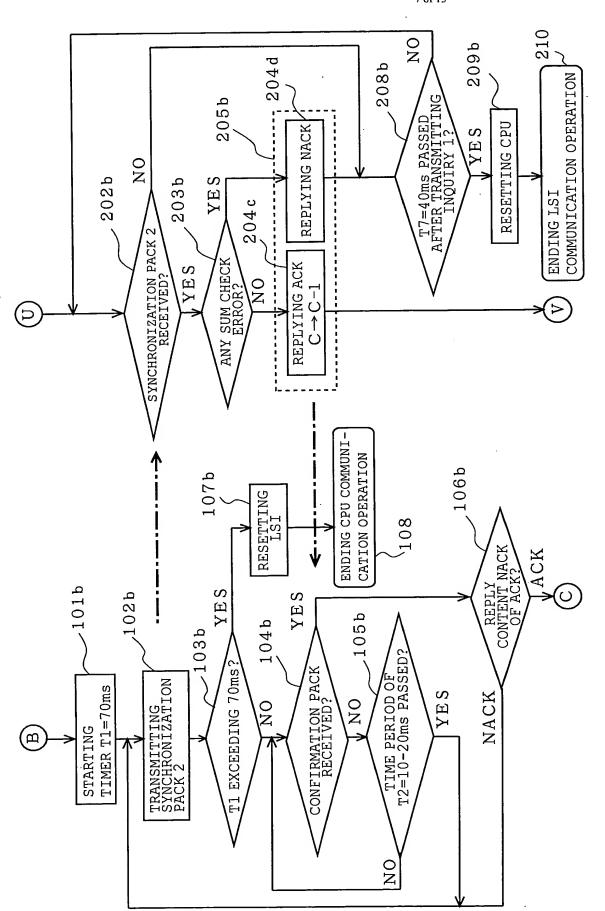
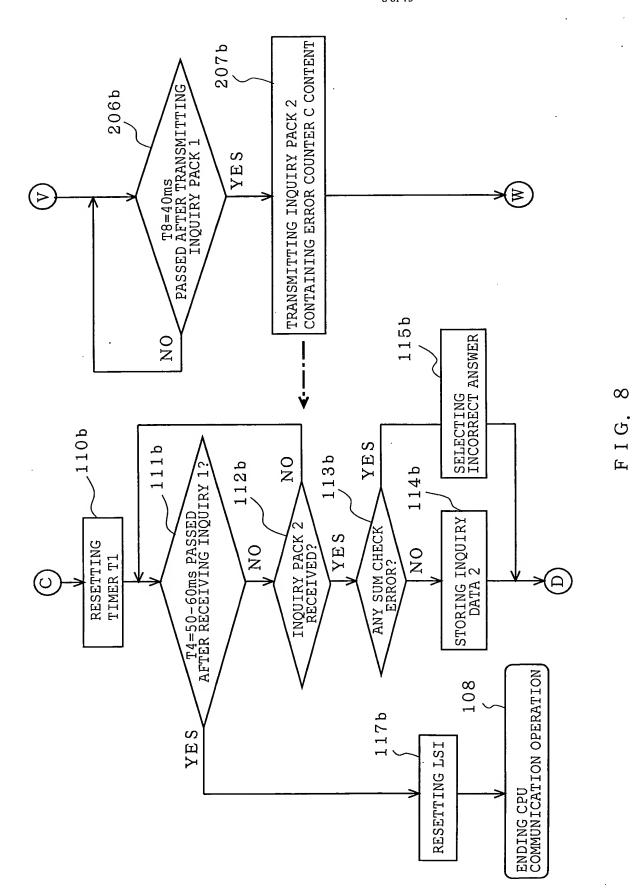
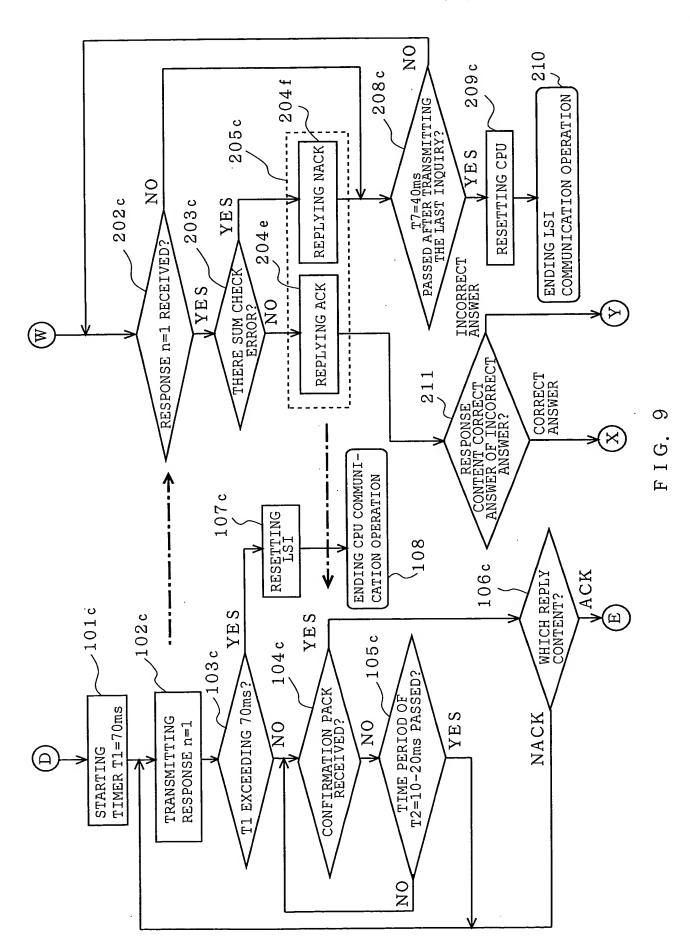


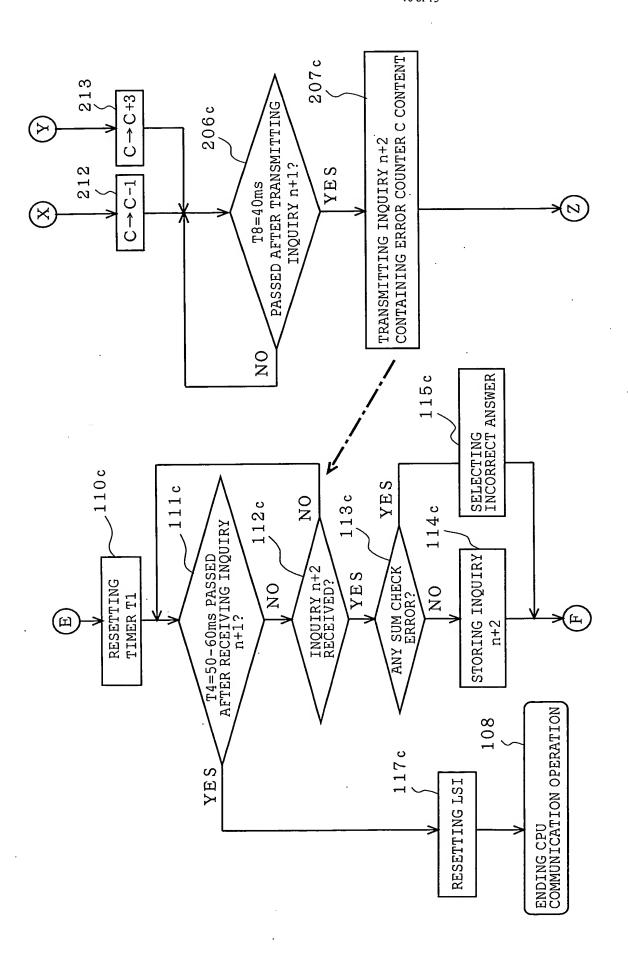
FIG. 7

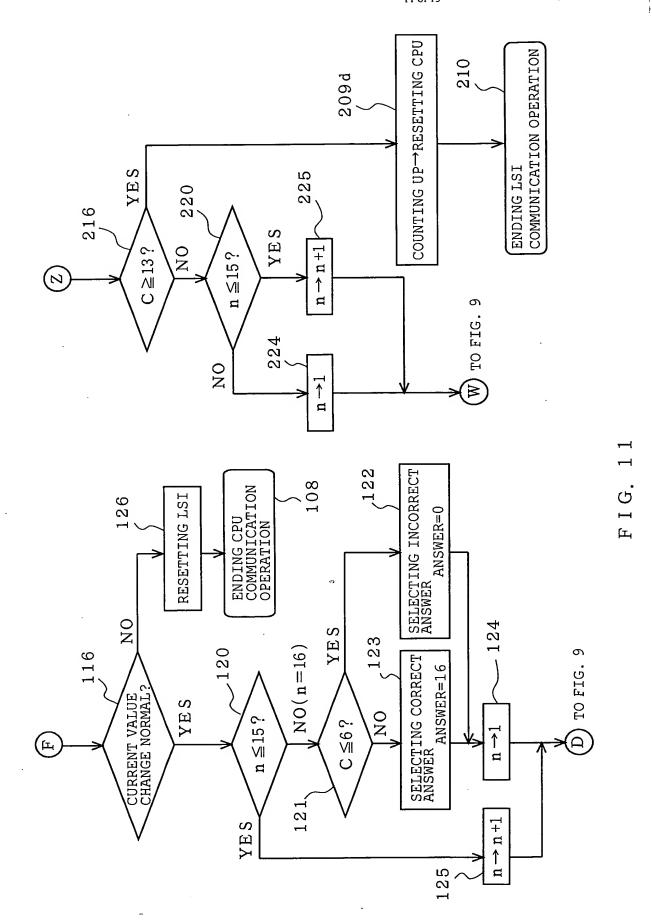


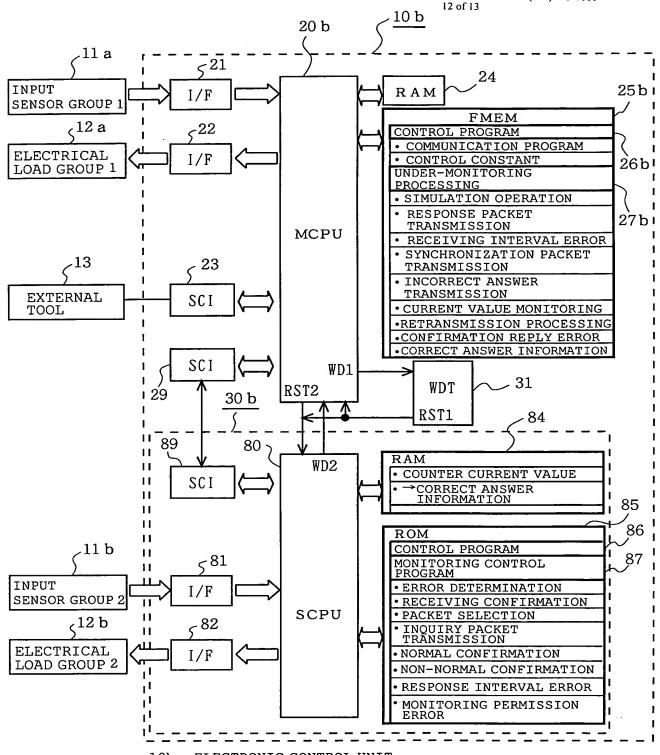


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10b : ELECTRONIC CONTROL UNIT 25b : NON-VOLATILE PROGRAM MEMORY

29, 49: SERIAL INTERFACE CIRCUIT

30b: MONITORING CONTROL CIRCUIT SECTION

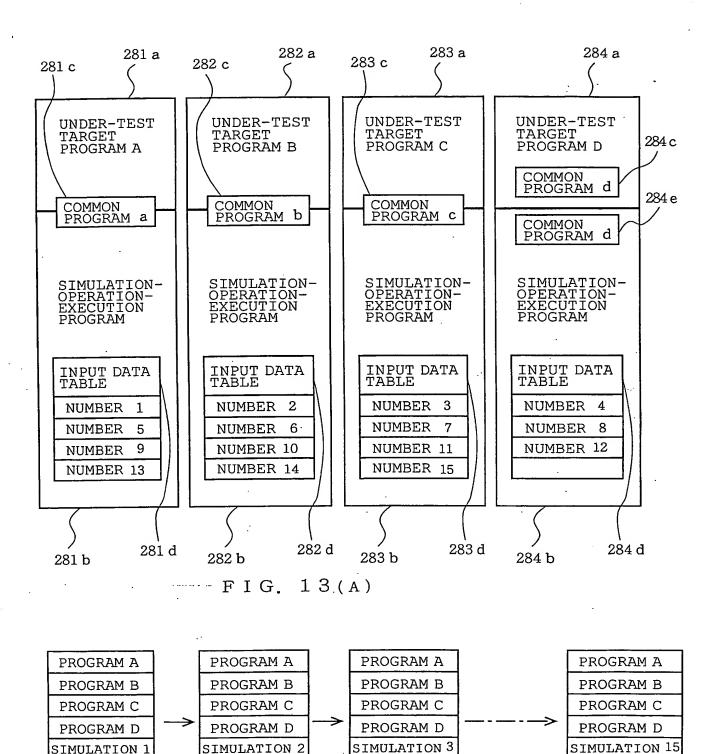
31: WATCHDOG TIMER

80 : AUXILIARY MICROPROCESSOR

84: AUXILIARY RAM MEMORY 85: AUXILIARY PROGRAM MEMORY

OTHER

**PROGRAMS** 



281b-284b: SIMULATION-OPERATION-EXECUTION PROCESSING PROGRAM F I G. 13 (B)

OTHER

**PROGRAMS** 

OTHER

**PROGRAMS** 

OTHER

**PROGRAMS**